

SW3588S-核心板-V10

技术规格书

Bozz Technology

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前言

概述

本文档主要介绍 **SW3588S-核心板-V10** 基本功能特点和硬件特性、多功能硬件配置、软件调试操作使用方法，旨在帮助开发人员更快、更准确地使用 **SW3588S-核心板-V10** 进行应用开发，熟悉 **SW3588S-核心板-V10** 解决方案。

产品版本

本文档对应的产品版本如下：

产品名称	平台名称	产品版本
SW3588S-核心板-V10	RK3588S	B0-00

适用对象

本文档主要适用于以下工程师：

- 技术支持工程师
- 硬件开发工程师
- 嵌入式软件开发工程师
- 应用软件开发工程师
- 测试工程师

修订记录

修订记录累积了每次文档更新的说明。最新版本的文档包含以前说有文档版本的更新内容。

修订日期	版本号	作者	修订说明
2023-04-07	V1.0	CEN	初始发布

缩略语

缩略语包括文档中常用词组的简称。

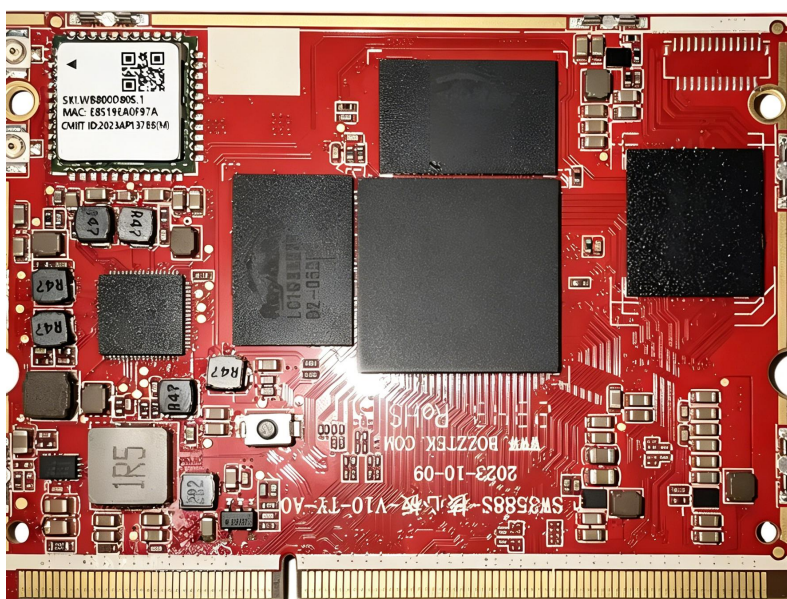
DDR	Double Data Rate	双倍速率同步动态随机存储器
eMMC	Embedded Multi Media Card	内嵌式多媒体存储卡
I ² C	Inter-Integrated Circuit	内部整合电路(两线式串行通讯总线)
JTAG	Joint Test Action Group	联合测试行为组织定义的一种国际标准测试协议 (IEEE 1149.1 兼容)
LDO	Low Drop Out Linear Regulator	低压差线性稳压器
LVDS	Low-Voltage Differential Signaling	低电压差分信号
MIPI	Mobile Industry Processor Interface	移动产业处理器接口
PMIC	Power Management IC	电源管理芯片
PMU	Power Management Unit	电源管理单元
RK	Rockchip Electronics Co.,Ltd.	瑞芯微电子股份有限公司
SD Card	Secure Digital Memory Card	安全数码卡
SDIO	Secure Digital Input and Output	安全数字输入输出接口
SDMMC	Secure Digital Multi Media Card	安全数字多媒体存储卡
TF Card	Micro SD Card (Trans-flash Card)	外置记忆卡
USB	Universal Serial Bus	通用串行总线

1. SW3588S-核心板-V10 外观尺寸

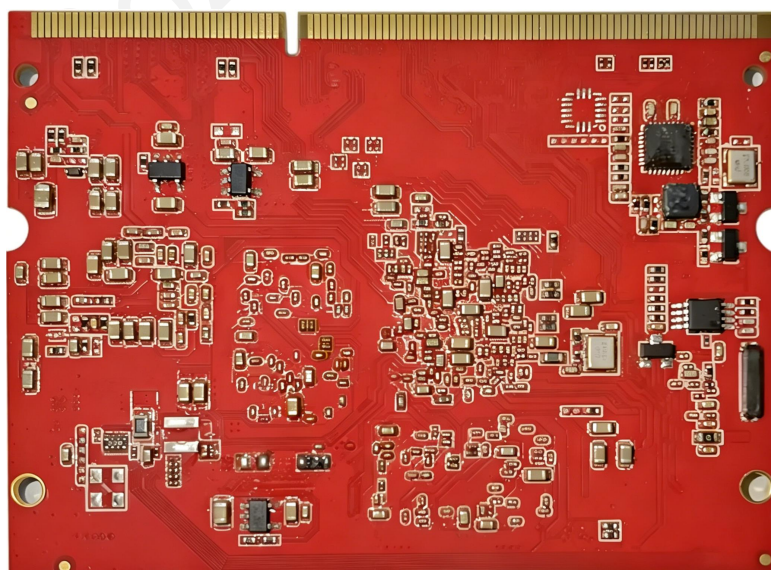
1.1 SW3588S-核心板-V10-通用-A0 外观尺寸

1.1.1 外观

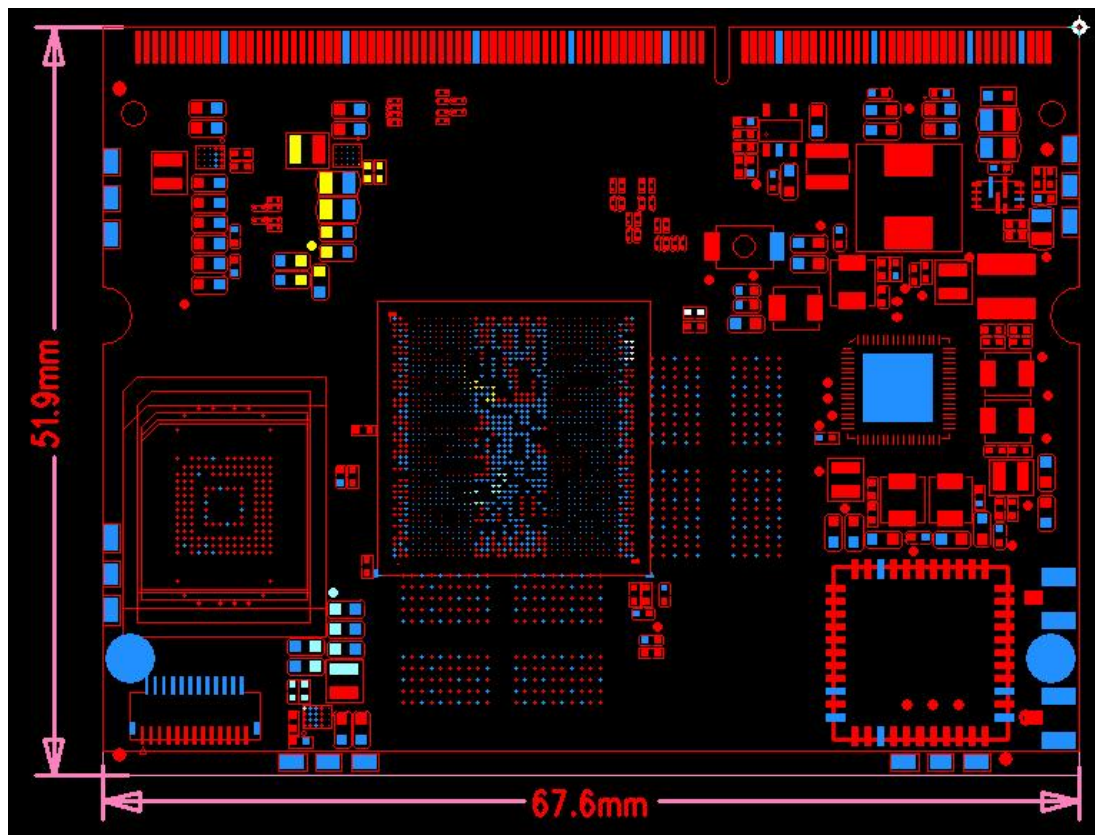
TOP:正面外观



BOTTOM: 背面外观



1.1.2 尺寸



尺寸：51.9 X 67.6 mm

PCB 厚度：1.2 mm

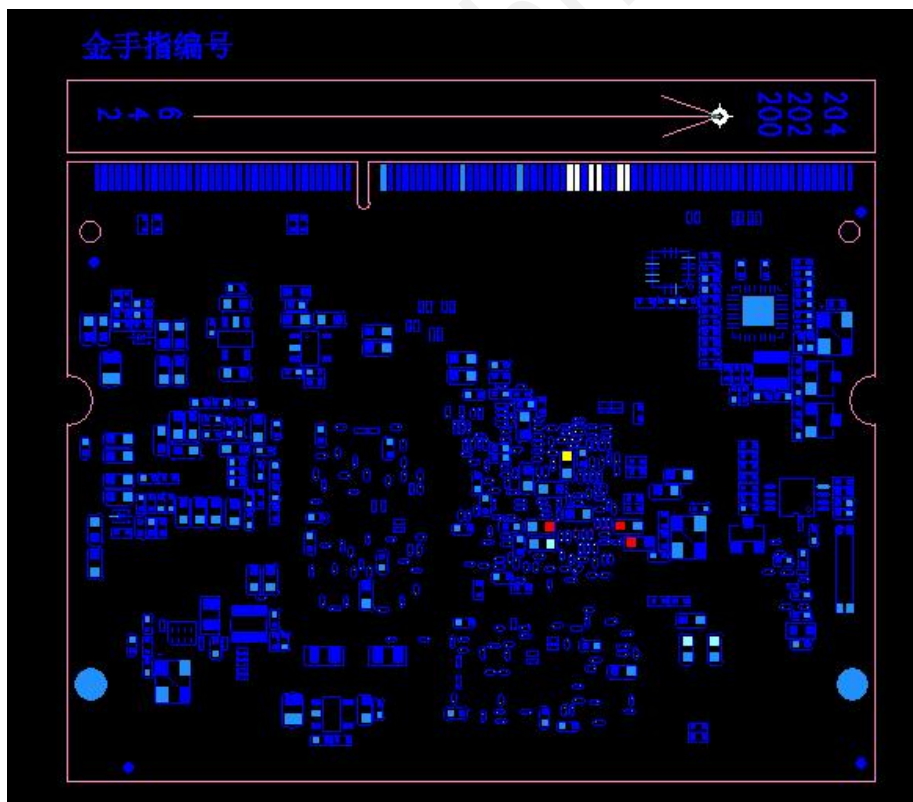
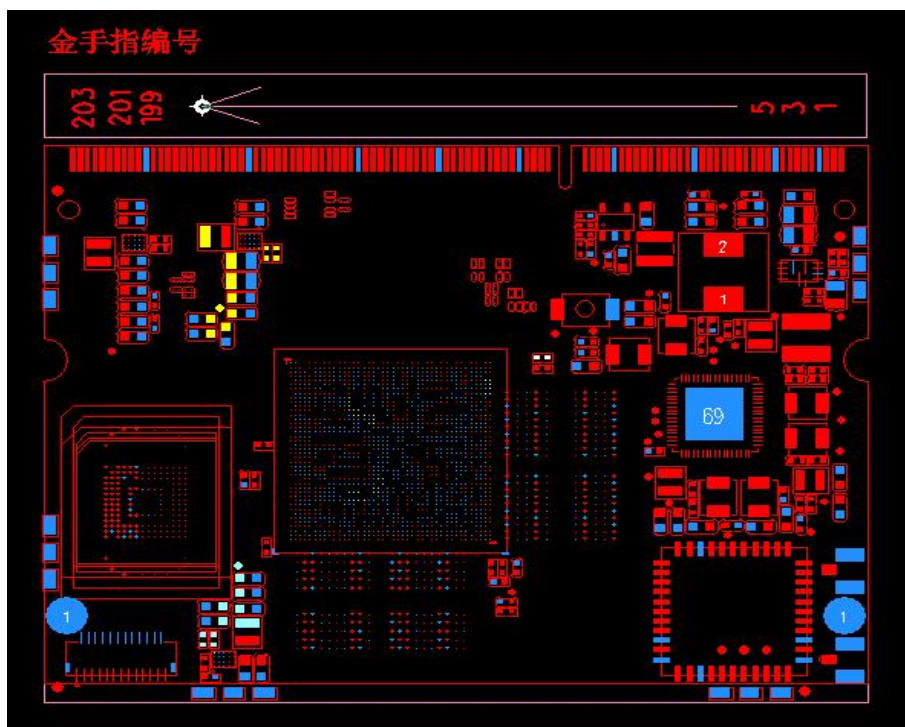
螺丝孔尺寸：Φ 2.0 mm

含器件整体厚度：3.5mm（根据选贴元器件不同而有差异）

2. SW3588S-核心板-V10 硬件规格

CPU	RK3588S
PMU	RK806-1
ROM	EMMC
RAM	LPDDR4
系统	Android/Linux
以太网	1000M
WIFI	IEEE 802.11 a/b/g/n/ac/ax 1T1R (WIFI6) 单天线
蓝牙	BT 5.2
USB	1*TYPEC+2*USB20 HOST+1 x USB3.0 (与 PCIE 复用)
HDMI	HDMI2.0
串口	串口*3
TF 卡	支持
PCIE	可选
audio	支持
MIPI CSI	2lane MIPI RX*2+4lane MIPI CSI
MIPI DSI	2*4lane MIPI DSI
EDP	4lane EDP 可选

3. SW3588S-核心板-V10 接口说明



CON1: 金手指定义

1	9V<VIN<19V
2	VCC_SYS_4.0V_OUT
3	9V<VIN<19V
4	I2S0_SDO3/I2S0_SDI2/PDM0_SDI2_M0/I2C1_SCL_M4/UART4_TX_M0/PWM0_M1/SPI1_CLK_M2/GPIO1_D2_d
5	9V<VIN<19V
6	I2S0_SDI1/PDM0_SDI3_M0/I2C1_SDA_M4/UART4_RX_M0/PWM1_M1/SPI1_CS0_M2/GPIO1_D3_d
7	GND
8	VCC_3V3_S0_OUT
9	I2C3_SDA_M0/UART3_RX_M0/SPI4_MISO_M0/GPIO1_C0_z
10	LCD0_VCC_OUT
11	PDM0_CLK0_M0/I2C4_SDA_M4/PWM15_IR_M2/GPIO1_C6_d
12	VCCA3V0_CODEC_OUT
13	PDM0_CLK1_M0/I2C2_SDA_M3/PWM11_IR_M2/SPI4_CS1_M0/GPIO1_C4_d
14	VCC_3V3_S0_OUT
15	CIF_D3/BT1120_D3/I2S1_SCLK_M0/DDRPHY_CH0_DTB_3/UART0_TX_M2/GPIO4_A3_d
16	/
17	PCIE20X1_1_WAKEN_M2/I2C2_SCL_M4/UART6_TX_M1/SPI4_MOSI_M2/GPIO1_A1_d
18	TP_VCC3V0_OUT
19	GND
20	VCC_3V3_S3_OUT
21	MIPI_DPHY1_TX_D0P/MIPI_CPHY1_TX_TRIO0_B
22	PMIC_RESET_L
23	MIPI_DPHY1_TX_D0N/MIPI_CPHY1_TX_TRIO0_A
24	RTC_IN
25	MIPI_DPHY1_TX_D1P/MIPI_CPHY1_TX_TRIO1_A
26	PWRON_L
27	MIPI_DPHY1_TX_D1N/MIPI_CPHY1_TX_TRIO0_C
28	VCC_1V8_S0_OUT
29	MIPI_DPHY1_TX_CLKP/MIPI_CPHY1_TX_TRIO1_C
30	CIF_CLKOUT/BT1120_D10/I2S1_SDO3_M0/DP0_HPDI0_M0/SPDIF0_TX_M1/DDRPHY_CH3_DTB0/UART9_TX_M1/PWM11_IR_M1/GPIO4_B4_u
31	MIPI_DPHY1_TX_CLKN/MIPI_CPHY1_TX_TRIO1_B
32	BT1120_D11/DDRPHY_CH3_DTB1/UART9_RX_M1/PWM12_M1/SPI3_MISO_M1/GPIO4_B5_d
33	MIPI_DPHY1_TX_D2P/MIPI_CPHY1_TX_TRIO2_B
34	BT1120_D12/SATA0_ACT_LED_M0/DDRPHY_CH3_DTB2/I2C5_SCL_M1/PWM13_M1/SPI3_MOSI_M1/GPIO4_B6_d

35	MIPI_DPHY1_TX_D2N/MIPI_CPHY1_TX_TRIO2_A
36	CIF_D2/BT1120_D2/I2S1_LRCK_M0/PCIE20X1_1_PERSTN_M1/DDRPHY_CH0_DTB2/SPI0_CLK_M1/GPI O4_A2_d
37	MIPI_DPHY1_TX_D3P/NO_USE
38	PDM1_SDI1_M1/SPI2_CS1_M0/GPIO1_B0_u
39	MIPI_DPHY1_TX_D3N/MIPI_CPHY1_TX_TRIO2_C
40	PDM1_SDI2_M1/SPI0_MISO_M2/GPIO1_B1_d
41	GND
42	I2S0_SDO0/I2C4_SCL_M4/UART4_CTSN/GPIO1_C7_d
43	MIPI_CSI0_D3P
44	I2S0_SDI0/GPIO1_D4_d
45	MIPI_CSI0_D3N
46	I2C3_SCL_M0/UART3_TX_M0/SPI4_MOSI_M0/GPIO1_C1_z
47	MIPI_CSI0_D2P
48	I2S0_LRCK/I2C2_SCL_M3/UART4_RTSN/GPIO1_C5_d
49	MIPI_CSI0_D2N
50	I2S0_SCLK/I2C6_SCL_M1/UART3_CTSN/PWM7_IR_M2/SPI4_CS0_M0/GPIO1_C3_d
51	MIPI_CSI0_CLK0P
52	I2S0_MCLK/I2C6_SDA_M1/UART3_RTSN/PWM3_IR_M2/SPI4_CLK_M0/GPIO1_C2_d
53	MIPI_CSI0_CLK0N
54	I2S0_SDO2/I2S0_SDI3/PDM0_SDI1_M0/I2C7_SDA_M0/UART6_RX_M2/SPI1_MOSI_M2/GPIO1_D1_d
55	MIPI_CSI0_D1P
56	I2S0_SDO1/I2C7_SCL_M0/UART6_TX_M2/SPI1_MISO_M2/GPIO1_D0_d
57	MIPI_CSI0_D1N
58	BT1120_D14/PCIE20X1_2_WAKEN_M1/HDMI_TX0_SDA_M0/I2C8_SCL_M3/SPI3_CS0_M1/GPIO4_C0_ u
59	MIPI_CSI0_D0P
60	BT1120_D13/PCIE20X1_2_CLKREQN_M1/HDMI_TX0_SCL_M0/DDRPHY_CH3_DTB3/I2C5_SDA_M1/SP I3_CLK_M1/GPIO4_B7_u
61	MIPI_CSI0_D0N
62	CIF_D10/SPI3_MISO_M3/GPIO3_C6_u
63	GND
64	BT1120_D15/SPDIF1_TX_M2/PCIE20X1_2_PERSTN_M1/HDMI_TX0_CEC_M0/I2C8_SDA_M3/PWM6_M 1/SPI3_CS1_M1/GPIO4_C1_d
65	HDMI_TX0_D3P/EDP_TX0_D3P
66	HDMI_TX0_HPD_M0/SPI2_MOSI_M0/GPIO1_A5_d
67	HDMI_TX0_D3N/EDP_TX0_D3N

68	SARADC_IN0_BOOT
69	HDMI_TX0_D0P/EDP_TX0_D0P
70	GMAC1_MDIO/MIPI_TE1/I2C8_SDA_M4/UART7_CTSN_M1/PWM15_IR_M0/SPI1_CS1_M1/GPIO3_C3_d
71	HDMI_TX0_D0N/EDP_TX0_D0N
72	GMAC1_MDC/MIPI_TE0/I2C8_SCL_M4/UART7_RTSN_M1/PWM14_M0/SPI1_CS0_M1/GPIO3_C2_d
73	HDMI_TX0_D1P/EDP_TX0_D1P
74	GND
75	HDMI_TX0_D1N/EDP_TX0_D1N
76	MIPI_DPHY0_TX_D3N/MIPI_CPHY0_TX_TRIO2_C
77	HDMI_TX0_D2P/EDP_TX0_D2P
78	MIPI_DPHY0_TX_D3P/NO_USE
79	HDMI_TX0_D2N/EDP_TX0_D2N
80	MIPI_DPHY0_TX_D2N/MIPI_CPHY0_TX_TRIO2_A
81	GND
82	MIPI_DPHY0_TX_D2P/MIPI_CPHY0_TX_TRIO2_B
83	TYPEC0_SBU1/DP0_AUXP
84	MIPI_DPHY0_TX_CLKN/MIPI_CPHY0_TX_TRIO1_B
85	TYPEC0_SBU2/DP0_AUXN
86	MIPI_DPHY0_TX_CLKP/MIPI_CPHY0_TX_TRIO1_C
87	TYPEC0_SSRX1P/DP0_TX0P
88	MIPI_DPHY0_TX_D1N/MIPI_CPHY0_TX_TRIO0_C
89	TYPEC0_SSRX1N/DP0_TX0N
90	MIPI_DPHY0_TX_D1P/MIPI_CPHY0_TX_TRIO1_A
91	TYPEC0_SSTX1N/DP0_TX1N
92	MIPI_DPHY0_TX_D0N/MIPI_CPHY0_TX_TRIO0_A
93	TYPEC0_SSTX1P/DP0_TX1P
94	MIPI_DPHY0_TX_D0P/MIPI_CPHY0_TX_TRIO0_B
95	TYPEC0_SSRX2P/DP0_TX2P
96	GND
97	TYPEC0_SSRX2N/DP0_TX2N
98	CIF_D1/BT1120_D1/I2S1_SCLK_M0/PCIE20X1_1_WAKEN_M1/DDRPHY_CH0_DTB_1/UART9_CTSN_M1/SPI0_MOSI_M1/GPIO4_A1_d
99	TYPEC0_SSTX2N/DP0_TX3N
100	CIF_D0/BT1120_D0/I2S1_MCLK_M0/PCIE20X1_1_CLKREQN_M1/DDRPHY_CH0_DTB0/UART9_RTSN_M1/SPI0_MISO_M1/GPIO4_A0_d
101	TYPEC0_SSTX2P/DP0_TX3P
102	TYPEC0_USB20_OTG_DM
103	GND
104	TYPEC0_USB20_OTG_DP
105	PCIE20_2_RXP/SATA30_2_RXP/USB30_2_SSRXP

106	TYPEC0_USB20_OTG_ID
107	PCIE20_2_RXN/SATA30_2_RXN/USB30_2_SSRXN
108	MIPI_DPHY1_RX_CLKN/MIPI_CPHY1_RX_TRIO1_B
109	PCIE20_2_TXN/SATA30_2_TXN/USB30_2_SSTXN
110	MIPI_DPHY1_RX_CLKP/MIPI_CPHY1_RX_TRIO1_C
111	PCIE20_2_TXP/SATA30_2_TXP/USB30_2_SSTXP
112	GND
113	MIPI_DPHY1_RX_D3N/MIPI_CPHY1_RX_TRIO2_C
114	/
115	MIPI_DPHY1_RX_D3P/NO_USE
116	SARADC_IN3
117	MIPI_DPHY1_RX_D2N/MIPI_CPHY1_RX_TRIO2_A
118	SARADC_IN1
119	MIPI_DPHY1_RX_D2P/MIPI_CPHY1_RX_TRIO2_B
120	TYPEC0_USB20_VBUSDET
121	MIPI_DPHY1_RX_D1N/MIPI_CPHY1_RX_TRIO0_C
122	MIPI_DPHY1_RX_D0N/MIPI_CPHY1_RX_TRIO0_A
123	MIPI_DPHY1_RX_D1P/MIPI_CPHY1_RX_TRIO1_A
124	MIPI_DPHY1_RX_D0P/MIPI_CPHY1_RX_TRIO0_B
125	GND
126	SDMMC_D2/PDM1_SDI1_M0/JTAG_TCK_M0/I2C8_SCL_M0/UART5_CTSN_M0/GPIO4_D2_u
127	MIPI_CSI0_CLK1P
128	SDMMC_D0/PDM1_SDI3_M0/JTAG_TCK_M1/I2C3_SCL_M4/UART2_TX_M1/PWM8_M1/GPIO4_D0_u
129	MIPI_CSI0_CLK1N
130	CIF_VSYNC/BT1120_D9/I2S1_SDO2_M0/PCIE20X1_2_BUTTON_RSTN/DDRPHY_CH2_DTB3/I2C7_SDA_M3/UART8_CTSN_M0/PWM15_IR_M1/CAN1_TX_M1/GPIO4_B3_u
131	MIPI_CAMERA2_CLK_M0/SPDIF1_TX_M0/SATA2_ACT_LED_M1/I2C5_SDA_M3/UART1_RX_M1/PWM13_M2/GPIO1_B7_u
132	SDMMC_D1/PDM1_SDI2_M0/JTAG_TMS_M1/I2C3_SDA_M4/UART2_RX_M1/PWM9_M1/GPIO4_D1_u
133	MIPI_CAMERA1_CLK_M0/SPDIF0_TX_M0/I2C5_SCL_M3/UART1_TX_M1/GPIO1_B6_u
134	SDMMC_CLK/PDM1_CLK0_M0/TEST_CLKOUT_M0/MCU_JTAG_TMS_M0/CAN0_RX_M1/UART5_TX_M0/GPIO4_D5_d
135	PCIE20_2_RXP/SATA30_2_RXP/USB30_2_SSRXP/GMAC1_TXEN/I2S2_SCLK_M1/CAN1_RX_M0/UART3_TX_M1/PWM12_M0/GPIO3_B5_u
136	SDMMC_DET/GPIO0_A4_u
137	PCIE20_2_RXN/SATA30_2_RXN/USB30_2_SSRXN/GMAC1_MCLKINOUT/I2S2_LRCK_M1/CAN1_TX_M0/UART3_RX_M1/PWM13_M0/GPIO3_B6_d
138	GMAC1_RXD0/MIPI_CAMERA2_CLK_M1/PWM8_M0/GPIO3_A7_u
139	PCIE20_2_TXP/SATA30_2_TXP/USB30_2_SSTXP/GMAC1_TXD0/I2S2_SDO_M1/UART2_RTSN/GPIO3_B3_u
140	SDMMC_D3/PDM1_SDI0_M0/JTAG_TMS_M0/I2C8_SDA_M0/UART5_RTSN_M0/PWM10_M1/GPIO4_

	D3_u
141	PCIE20_2_TXN/SATA30_2_TXN/USB30_2_SSTXN/SARADC_IN4
142	SDMMC_CMD/PDM1_CLK1_M0/MCU_JTAG_TCK_M0/CAN0_TX_M1/UART5_RX_M0/PWM7_IR_M1/GPIO4_D4_u
143	PCIE20_2_REFCLKN/GMAC1_TXER/I2S2_SDI_M1/UART2_RX_M2/PWM3_IR_M1/GPIO3_B2_d
144	I2S1_SDO3_M1/CPU_BIG1_AVS/I2C1_SDA_M2/CAN2_TX_M1/HDMI_TX0_SCL_M1/SPI3_CS1_M2/SATA_MP_SWITCH/GPIO0_D5_u
145	PCIE20_2_REFCLKP/SARADC_IN2
146	I2S1_SDI3_M1/PDM0_SDI1_M1/I2C6_SCL_M0/UART1_CTSN_M2/PWM7_IR_M0/SPI3_MISO_M2/GPIO0_D0_d
147	USB20_HOST0_DP
148	GMAC1_RXD1/MIPI_CAMERA3_CLK_M1/PWM9_M0/GPIO3_B0_u
149	USB20_HOST0_DM
150	PDM1_CLK1_M1/SATA0_ACT_LED_M1/UART4_TX_M2/SPI0_CLK_M2/GPIO1_B3_d
151	USB20_HOST1_DP
152	PDM1_SDI3_M1/UART4_RX_M2/SPI0_MOSI_M2/GPIO1_B2_d
153	USB20_HOST1_DM
154	PDM0_CLK1_M1/PWM2_M0/UART0_RX_M0/I2C4_SDA_M2/DP0_HPDIN_M1/GPIO0_C4_d
155	GND
156	CIF_HREF/BT1120_D8/I2S1_SDO1_M0/PCIE20X1_1_BUTTON_RSTN/DDRPHY_CH2_DTB2/I2C7_SCL_M3/UART8_RTSN_M0/PWM14_M1/SPI0_CS0_M1/CAN1_RX_M1/GPIO4_B2_u
157	PDM1_SDI0_M1/PCIE20X1_1_PERSTN_M2/PWM3_IR_M3/SPI2_CS0_M0/GPIO1_A7_u
158	CIF_CLKIN/BT1120_CLKOUT/I2S1_SDI3_M0/DDRPHY_CH2_DTB0/I2C6_SDA_M3/UART8_TX_M0/SPI2_CS1_M1/GPIO4_B0_d
159	PCIE20X1_1_CLKREQN_M2/DP0_HPDIN_M2/I2C2_SDA_M4/UART6_RX_M1/SPI4_MISO_M2/GPIO1_A0_d
160	MIPI_CAMERA0_CLK_M0/SPDIF1_TX_M1/I2S1_SDO0_M0/SATA2_ACT_LED_M0/DDRPHY_CH2_DTB1/I2C6_SCL_M3/UART8_RX_M0/SPI0_CS1_M1/GPIO4_B1_u
161	I2S1_MCLK_M1/JTAG_TCK_M2/I2C1_SCL_M0/UART2_TX_M0/PCIE20X1_1_CLKREQN_M0/GPIO0_B5_d
162	CIF_D9/FSPI_CS1N_M2/CAN2_TX_M0/UART5_RX_M1/SPI3_CS1_M3/GPIO3_C5_u
163	I2S1_SCLK_M1/JTAG_TMS_M2/I2C1_SDA_M0/UART2_RX_M0/PCIE20X1_1_WAKEN_M0/GPIO0_B6_d
164	CIF_D8/FSPI_CS0N_M2/CAN2_RX_M0/UART5_TX_M1/SPI3_CS0_M3/GPIO3_C4_u
165	PDM0_SDI0_M0/SPI1_CS1_M2/GPIO1_D5_d
166	UART7_TX_M2/SPI0_CS1_M2/GPIO1_B5_u
167	GMAC1_RXDV_CRS/MIPI_CAMERA4_CLK_M1/UART2_TX_M2/PWM2_M1/GPIO3_B1_d
168	PDM1_CLK0_M1/UART7_RX_M2/SPI0_CS0_M2/GPIO1_B4_u
169	GMAC1_RXDV_CRS/MIPI_CAMERA4_CLK_M1/UART2_TX_M2/PWM2_M1/GPIO3_B1_d
170	SPI2_MISO_M0/GPIO1_A4_d
171	GMAC1_PTP_REF_CLK/I2C3_SCL_M1/SPI1_MOSI_M1/GPIO3_B7_d
172	VOP_POST_EMPTY/I2C4_SDA_M3/UART6_RTSN_M1/PWM0_M2/SPI4_CLK_M2/GPIO1_A2_d
173	GMAC1_PPSTRIG/I2C3_SDA_M1/UART7_TX_M1/SPI1_MISO_M1/GPIO3_C0_d

174	I2C4_SCL_M3/UART6_CTSN_M1/PWM1_M2/SPI4_CS0_M2/GPIO1_A3_d
175	GMAC1_TXD1/I2S2_MCLK_M1/UART2_CTSN/GPIO3_B4_u
176	CIF_D4/BT1120_D4/DDRPHY_CH1_DTB_0/I2C3_SCL_M2/UART0_RX_M2/SPI2_MISO_M1/GPIO4_A4_d
177	GMAC1_PPSCCLK/UART7_RX_M1/SPI1_CLK_M1/GPIO3_C1_d
178	CIF_D7/BT1120_D7/I2S1_SDI2_M0/DDRPHY_CH1_DTB3/I2C5_SDA_M2/SPI2_CS0_M1/GPIO4_A7_d
179	MIPI_CAMERA4_CLK_M0/I2C8_SDA_M2/UART1_CTSN_M1/PWM15_IR_M3/GPIO1_D7_u
180	CIF_D6/BT1120_D6/I2S1_SDI1_M0/DDRPHY_CH1_DTB2/I2C5_SCL_M2/UART3_RX_M2/SPI2_CLK_M1/GPIO4_A6_d
181	MIPI_CAMERA3_CLK_M0/I2C8_SCL_M2/UART1_RTSN_M1/PWM14_M2/GPIO1_D6_u
182	CIF_D5/BT1120_D5/I2S1_SDI0_M0/DDRPHY_CH1_DTB_1/I2C3_SDA_M2/UART3_TX_M2/SPI2_MOSI_M1/GPIO4_A5_d
183	GND
184	SPI2_CLK_M0/GPIO1_A6_d
185	HDMI_TX0_D3N/EDP_TX0_D3N
186	MDI3-
187	HDMI_TX0_D3P/EDP_TX0_D3P
188	MDI3+
189	HDMI_TX0_D2N/EDP_TX0_D2N
190	MDI2-
191	HDMI_TX0_D2P/EDP_TX0_D2P
192	MDI2+
193	HDMI_TX0_D1P/EDP_TX0_D1P
194	MDI1-
195	HDMI_TX0_D1N/EDP_TX0_D1N
196	MDI1+
197	HDMI_TX0_D0P/EDP_TX0_D0P
198	MDI0-
199	HDMI_TX0_D0N/EDP_TX0_D0N
200	MDI0+
201	HDMI_TX0_SBDP/EDP_TX0_AUXP
202	LED1
203	HDMI_TX0_SBDN/EDP_TX0_AUXN
204	LED2

4.使用注意事项

- 1) 相对湿度: $\leq 80\%$;
- 2) 存储温度: $-0\sim 60^{\circ}\text{C}$;
- 3) 使用温度: $0\sim 50^{\circ}\text{C}$;
- 4) 请使板卡远离静电;
- 5) 勿受重压及弯折变形, 跌落;
- 6) 正确接好驱屏线前请勿接通电源;
- 7) 当板卡正在工作时切勿在板卡上掉入可导电物体;
- 8) 请勿拆解此板卡;
- 9) 如果板卡有灰尘, 请用干布擦试。